



Introducing Codasip

The Leader in Processor Design Automation

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Codasip Group

- Founded in 2014
 - In 2015 released the first commercial RISC-V core
- Verified, production-ready RISC-V cores

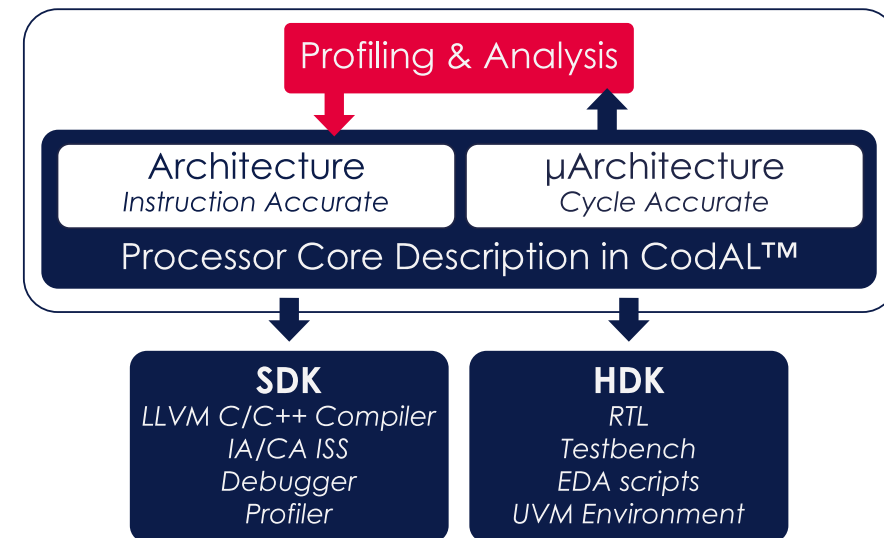


R&D in Europe, global Sales force

Your RISC-V Vendor and EDA Tools Provider

- Customize Cudasip RISC-V cores with Cudasip Studio™
- Modify both ISA and microarchitecture in CodAL™ architecture description language
 - ~3x efficient than traditional RTL approach
 - Unmatched customization capabilities
- Deliverables generated from a single description
 - Complete HW design, SW Tool chain, UVM, etc.
- Customize an open-source RISC-V core
 - Architecture exploration and SDK generation is faster with Cudasip Studio™

Design your own core or start with proven Cudasip IP



Looking forward to meeting you all

Come and see us at our
stand in the exhibition hall

We are excited to meet and talk
with everyone

**Customizing RISC-V designs
to unlock innovation
with Cudasip**

Wednesday – 2:25pm

Zdeněk Přikryl, CTO



Thank you.

Now, it's your turn!

www.codasip.com

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